

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				ATTY. DOCKET NO. SP038.C5 (1397.0140005)	APPLICATION NO. To Be Assigned	10/815,742	
				FIRST NAMED INVENTOR WANG et al.			
				FILING DATE April 2, 2004	ART UNIT To Be Assigned		
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
RLE	AA1	4,626,989	12/02/1986	Torii	—	—	—
RLE	AB1	4,675,806	06/23/1987	Uchida	—	—	—
RLE	AC1	4,722,049	01/26/1988	Lahti	—	—	—
RLE	AD1	4,807,115	02/21/1989	Torng	—	—	—
RLE	AE1	4,823,201	04/18/1989	Simon et al.	—	—	—
RLE	AF1	4,903,196	02/20/1990	Pomerene et al.	—	—	—
RLE	AG1	4,942,525	07/17/1990	Shintani et al.	—	—	—
RLE	AH1	5,067,069	11/19/1991	Fite et al.	—	—	—
RLE	AI1	5,072,364	12/10/1991	Jardine et al.	—	—	—
RLE	AJ1	5,109,495	04/28/1992	Fite et al.	—	—	—
RLE	AK1	5,125,083	06/23/1992	Fite et al.	—	—	—
FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
RLE	AL1	WO 88/09035	11/1988	PCF WIPO	—	—	Yes No
RLE	AM1	0 515 166	11/1992	EP EPO	—	—	Yes No
RLE	AN1	H2-48732	02/19/1990	Japan	—	—	Yes No
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)							
RLE	AO	1	Acosta, R. D. et al., "An Instruction Issuing Approach to Enhancing Performance in Multiple Functional Unit Processors," <i>IEEE Transactions On Computers</i> , IEEE, Vol. C-35, No. 9, pp. 815-828 (September 1986).				
RLE	AP	1	Agerwala, T. and Cocke, J., "High Performance Reduced Instruction Set Processors," IBM Research Division, pp. 1-61 (March 31, 1987).				
RLE	AR	1	Butler, M. and Fatt, Y., "An Improved Area-Efficient Register Alias Table for Implementing HPS," University of Michigan, Ann Arbor, Michigan, 24 pages (January 1990).				
RLE	AS	1	Butler, M. et al., "Single Instruction Stream Parallelism Is Greater than Two," <i>Proceedings of the 18th Annual International Symposium on Computer Architecture</i> , ACM, pp. 276-286 (May 1991).				
RLE	AT	1	Charlesworth, A.E., "An Approach to Scientific Array Processing: The Architectural Design of the AP-120B/FPS-164 Family," <i>Computer</i> , IEEE, Vol. 14, pp. 18-27 (September 1981).				
EXAMINER	Richard Ellis			DATE CONSIDERED 10/14/2004			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							